

PREVENT UNNECESSARY CONSTANT RECALL (TS-2 CABINETS ONLY)

Note: the following steps concern the write-protect area of the controller. Only

knowledgeable and trained personnel should modify values in the write-protect area.

>>Open the front panel of the controller

>>Ground the center pin of jumper plug 5 (JP5(B)), located just above the chip in position U-15 in the processor module (just below “Econolite” on the inside of the front panel):

A	JP5	B
VCC		GND

This disables the fault status output in the write protect area, which is fine, since this is outputting logic ground anyway. This step is required to modify the write-protect area.

>>From the main menu

- touch 8-Utilities

- touch 9-Write protect (hidden option)

>> Locate address location 007 – this will be found in the right-most column of the first row.

ADDRESS	0/8	1/9	2/A	3/B	4/C	5/D	6/E	7/F
000-007								XX

>> Enter “02” for “XX” in location 007 to ignore BIU detector status.

This turns bit 1 to “TRUE” but leaves all other bits (0, 2-7) “FALSE”. Doing so disables detection = TRUE and prevents constant recall from occurring regardless of the status of a minor RFE (open/closed/erratic/dead loop).

>>Remove the jumper from the processor module to prevent further write-protect changes.

NOTE: Additional explanation of write-protection entries appears in the appendix of this report.